

1/30

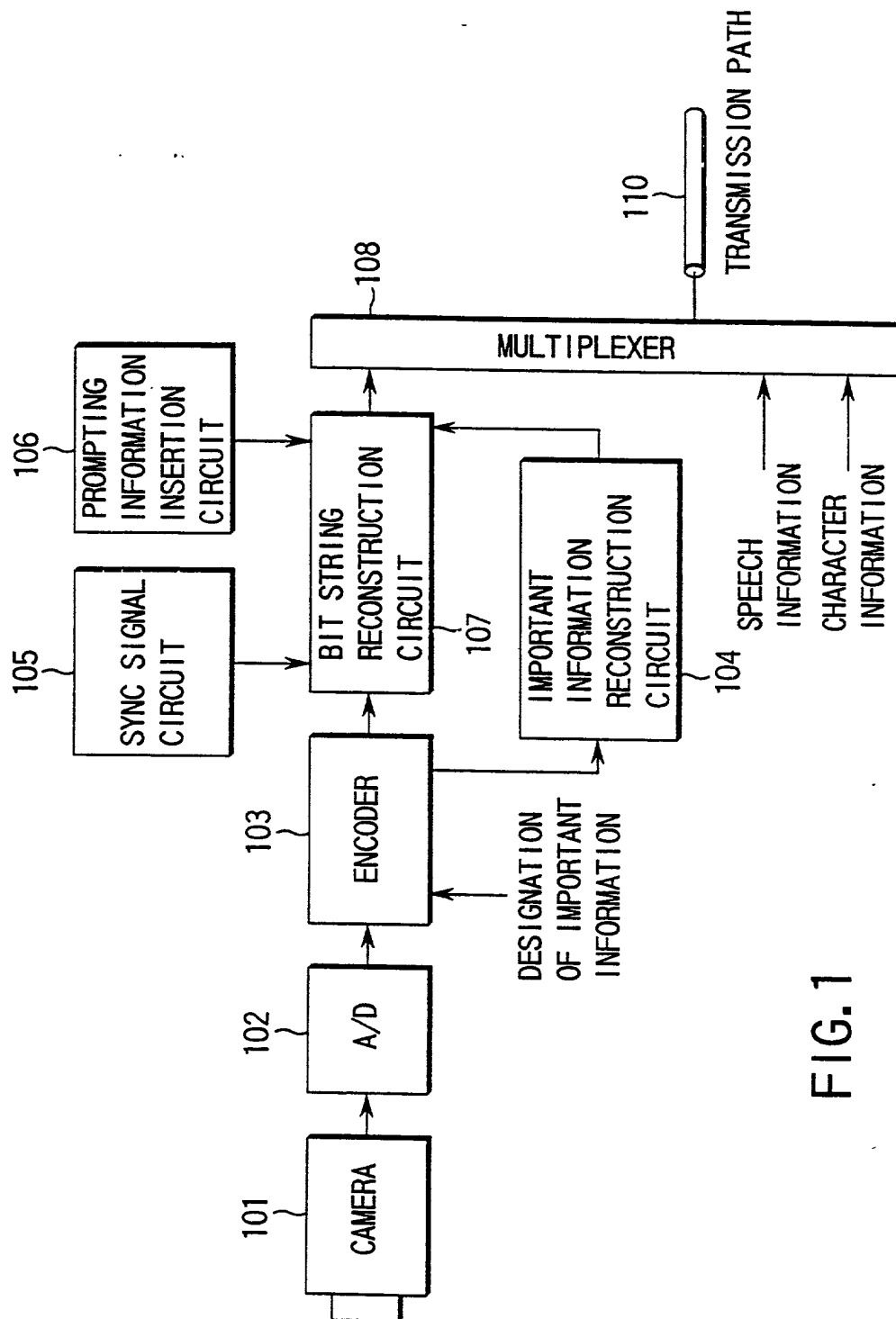


FIG. 1

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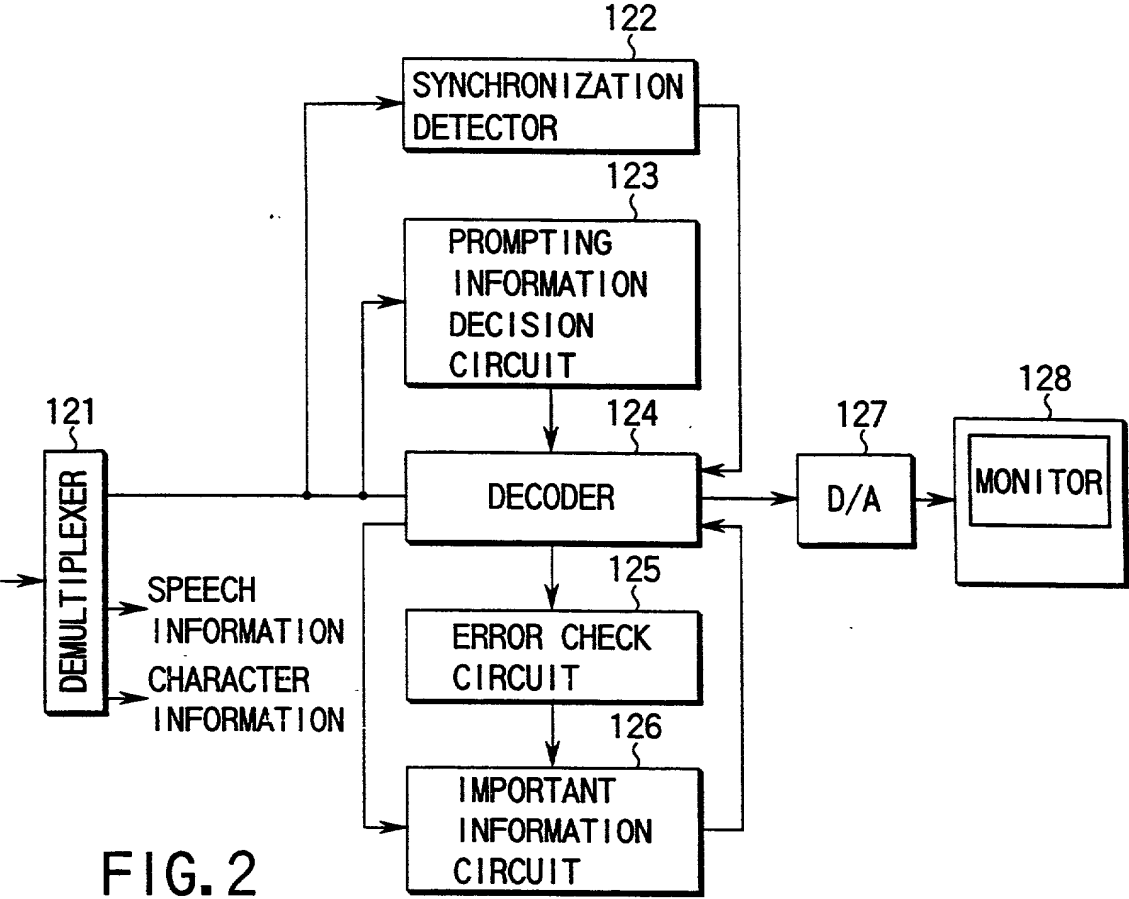


FIG. 2

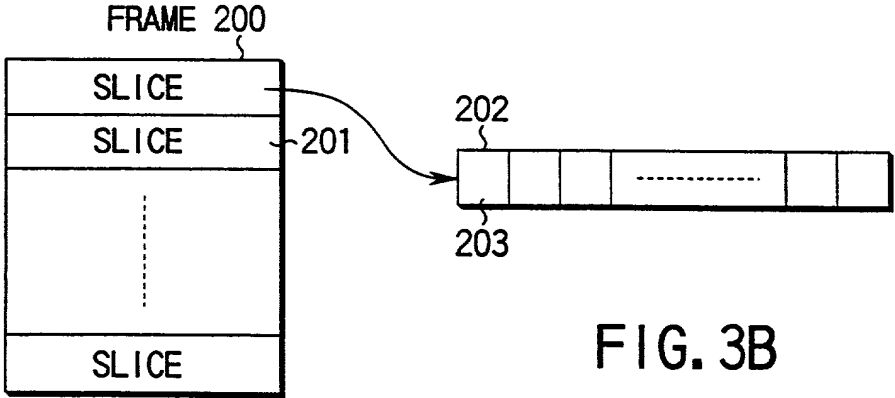


FIG. 3A

FIG. 3B

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FIG. 4A

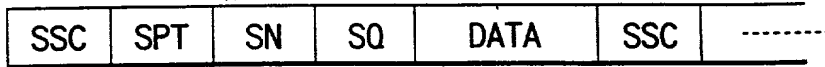


FIG. 4B PRIOR ART

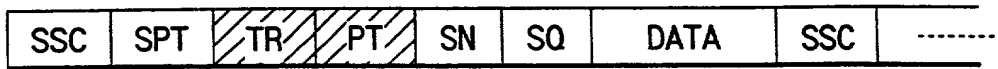


FIG. 4C

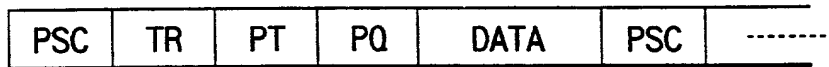


FIG. 5A



FIG. 5B

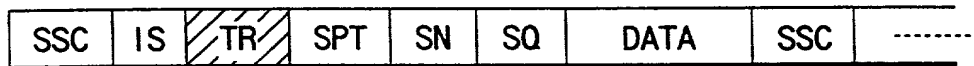


FIG. 5C



FIG. 5D

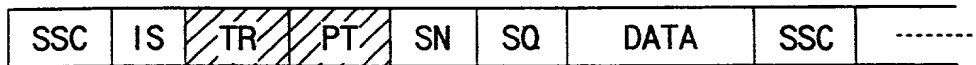


FIG. 5E

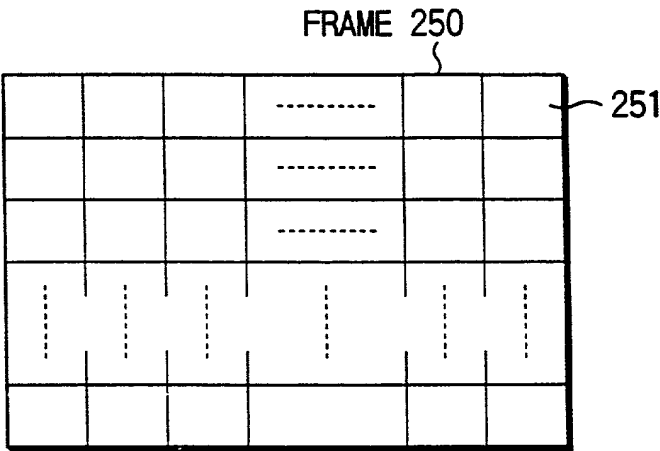


FIG. 6A



FIG. 6B

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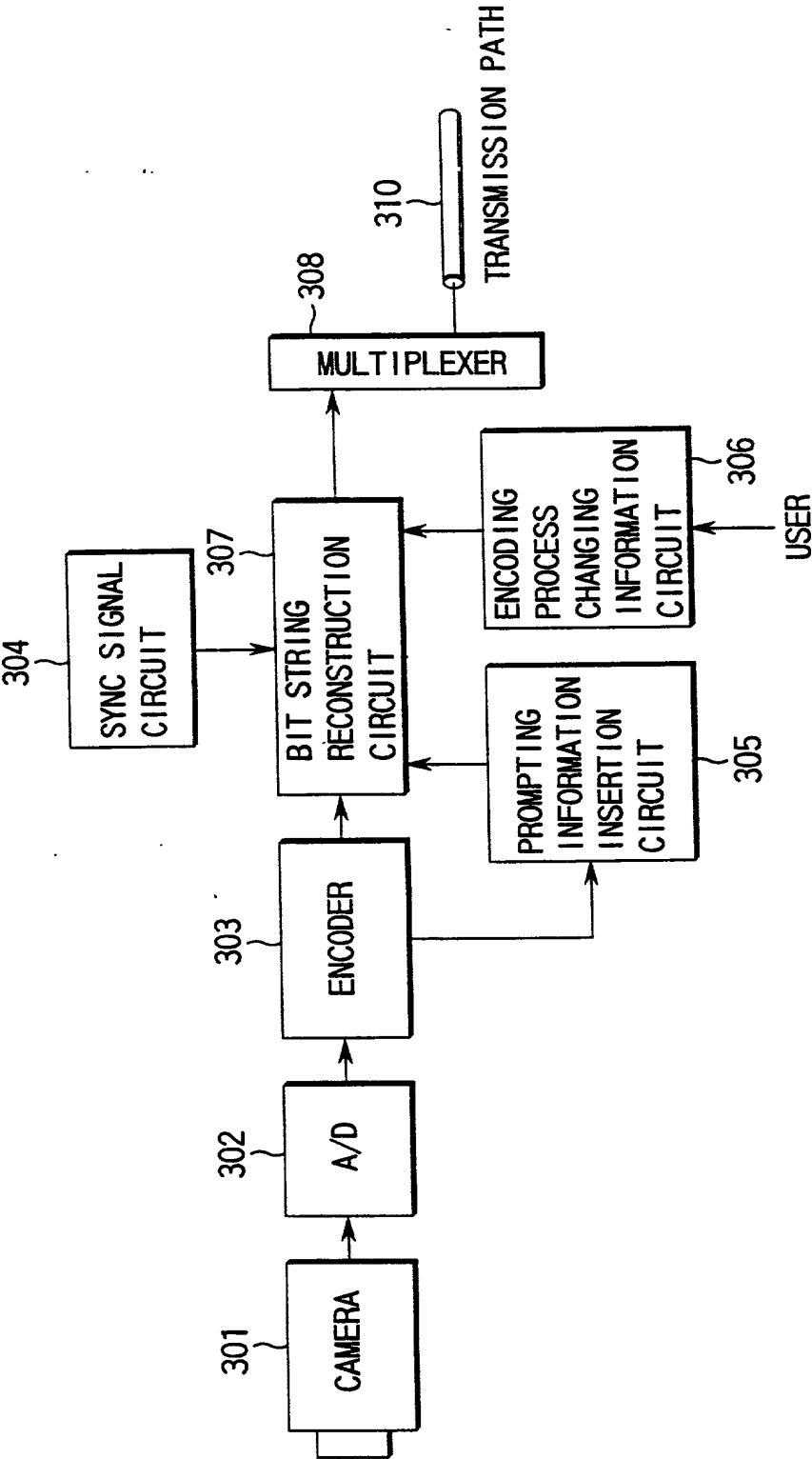


FIG. 7

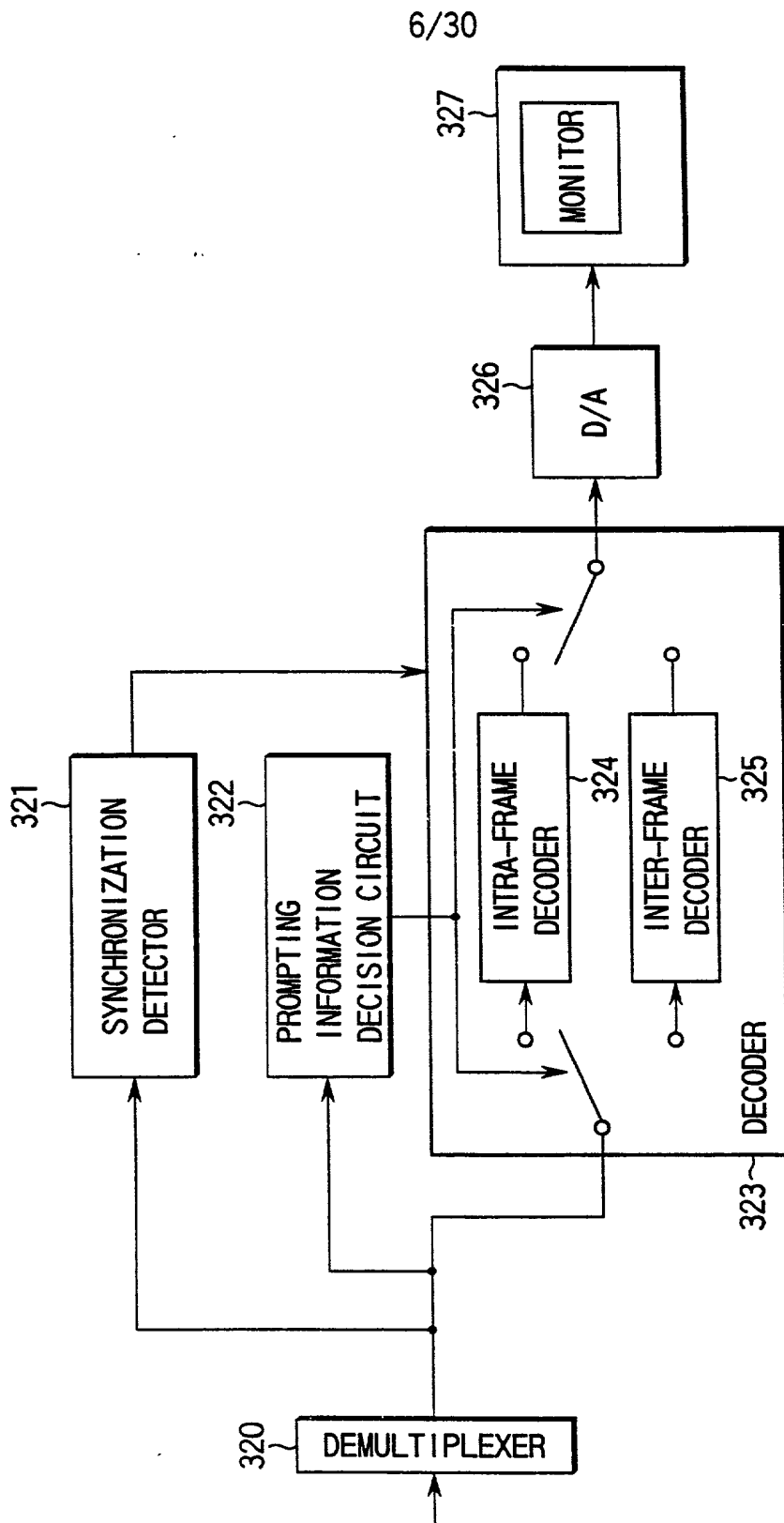


FIG. 8

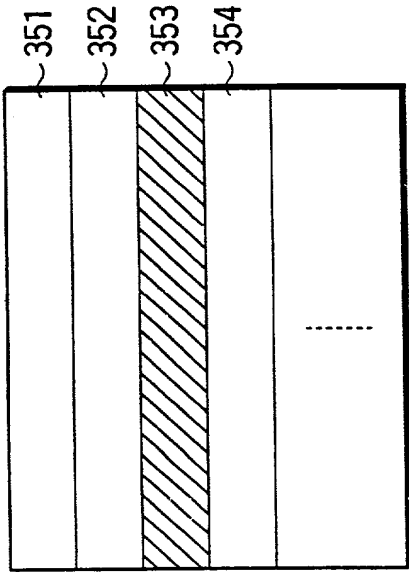


FIG. 9A

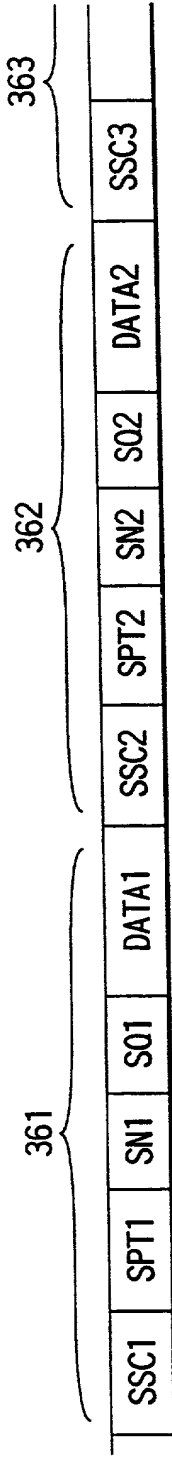


FIG. 9B



FIG. 10A

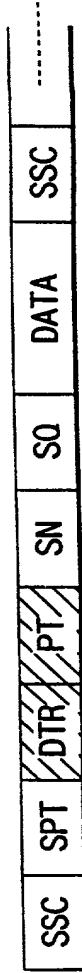


FIG. 10B

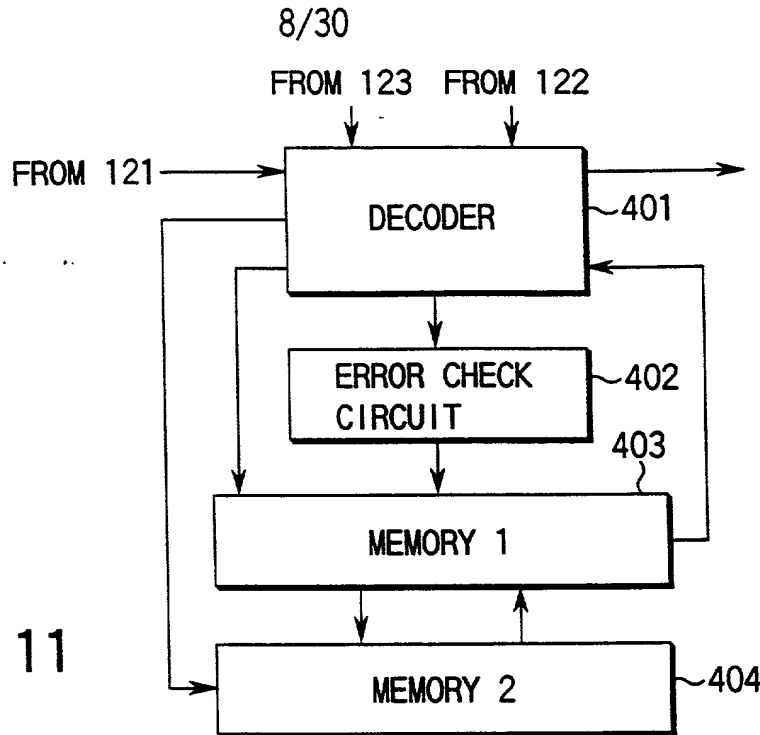


FIG. 11

CODE	PREDICTION TYPE
00	I
01	P
10	B
11	PROMPTING INFORMATION

FIG. 12

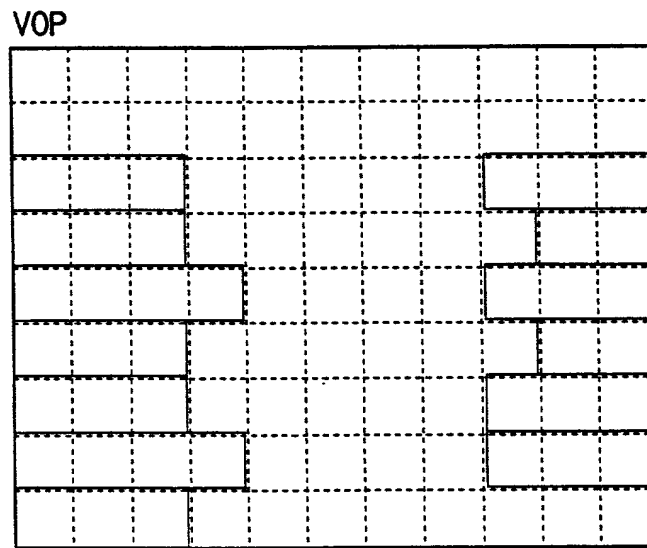


FIG. 13



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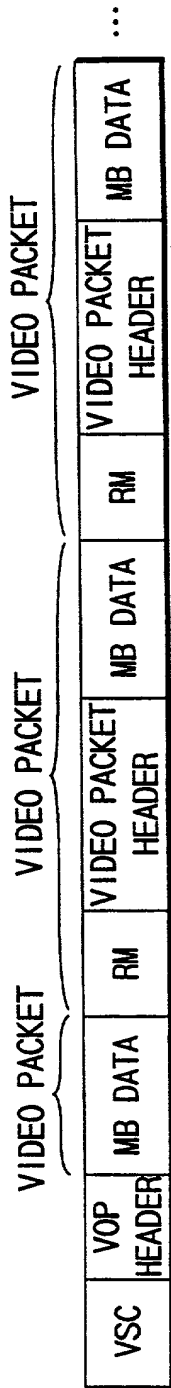


FIG. 14A

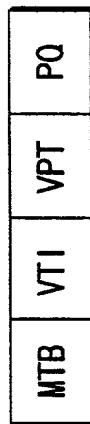


FIG. 14B



FIG. 14C



FIG. 14D

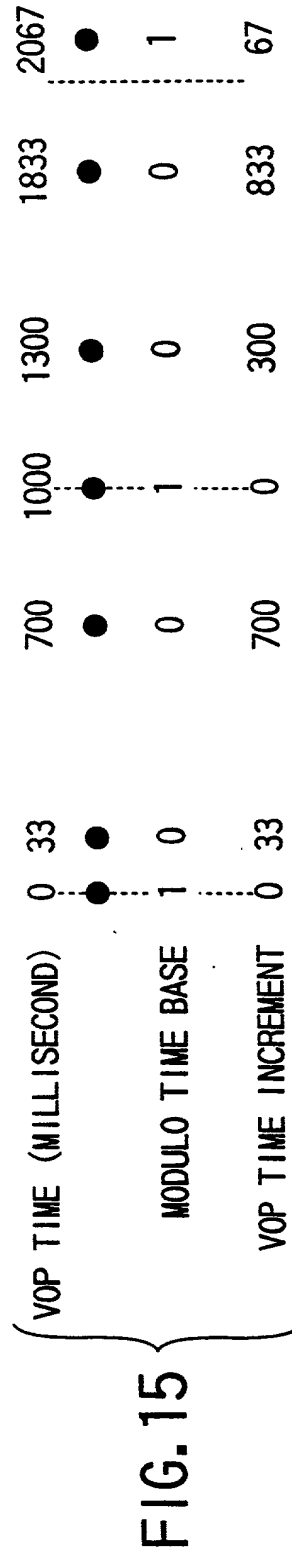


FIG. 15

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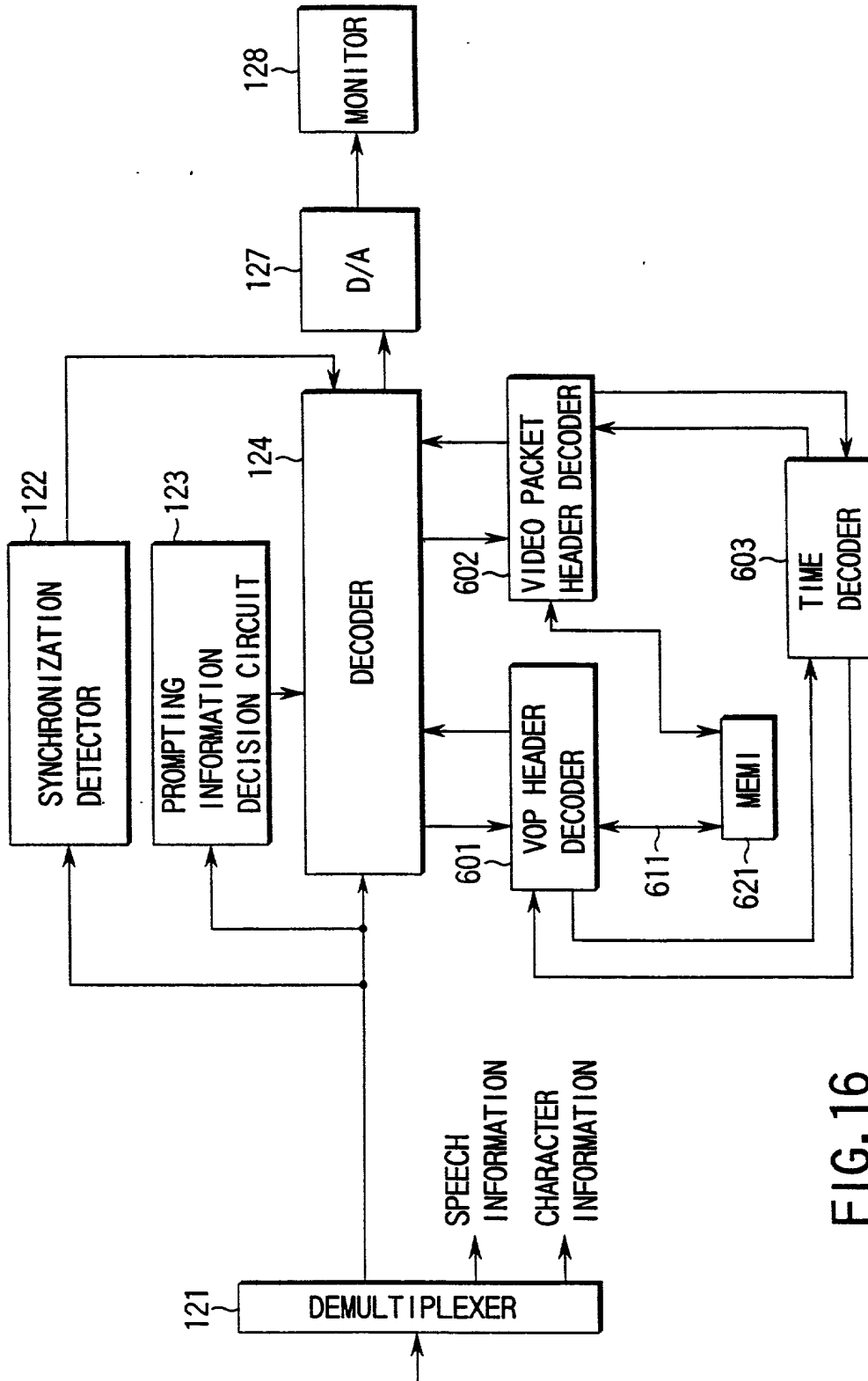


FIG. 16

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FIG. 17A



FIG. 17B

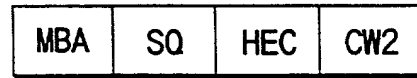


FIG. 17C



FIG. 19

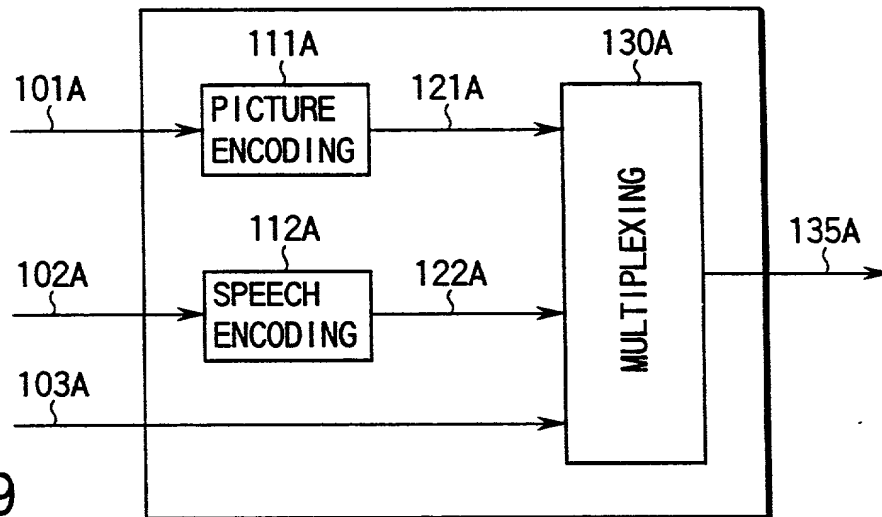
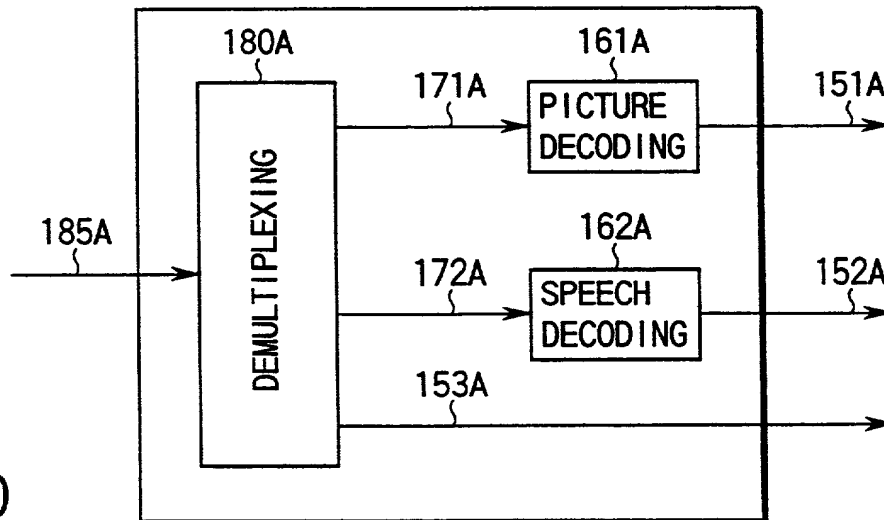


FIG. 20



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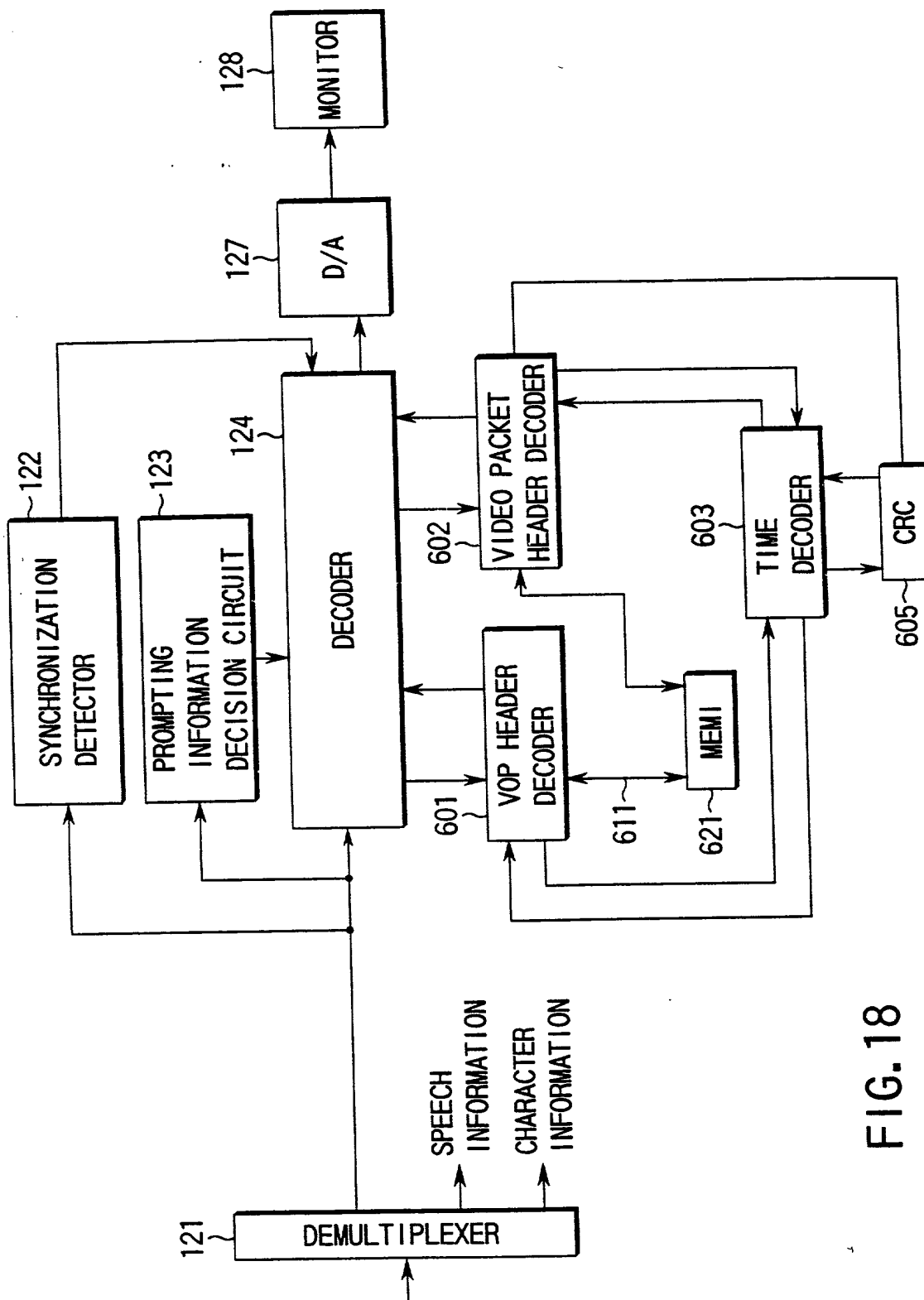


FIG. 18

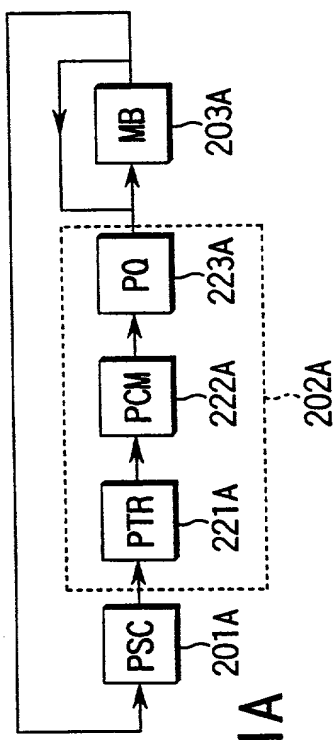


FIG. 21A

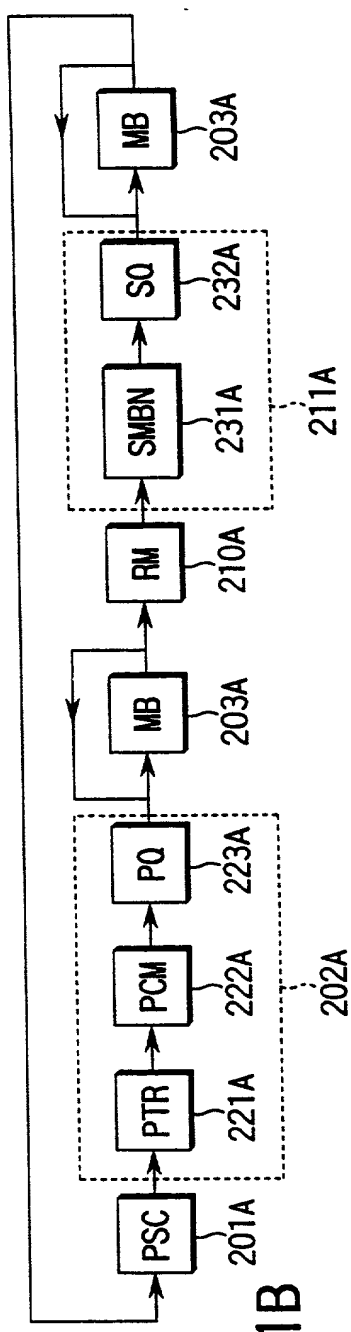


FIG. 21B

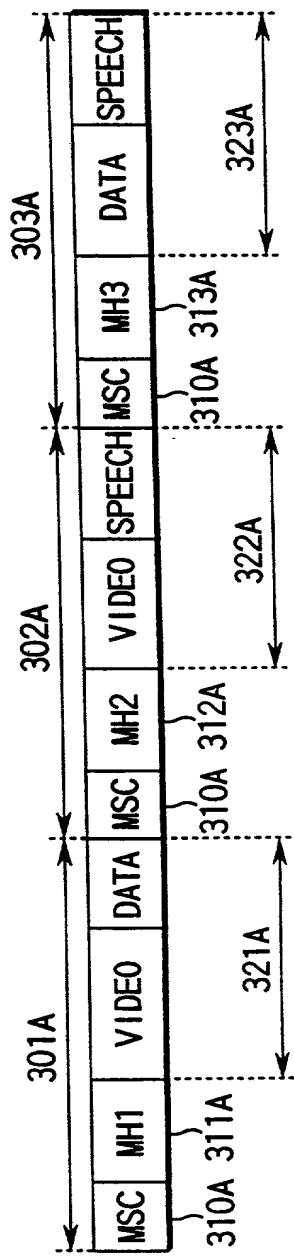


FIG. 22

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FIG. 23A

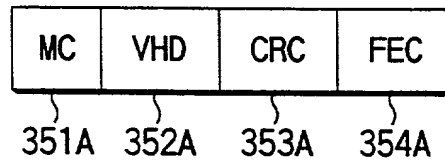


FIG. 23B

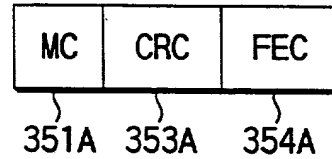


FIG. 24A

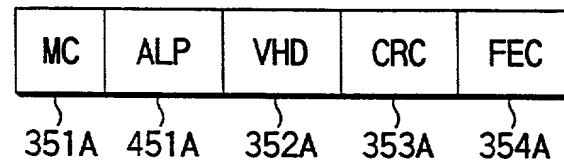


FIG. 24B

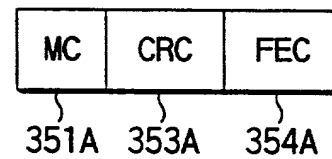


FIG. 27A

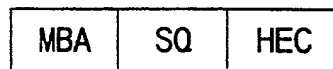


FIG. 27B

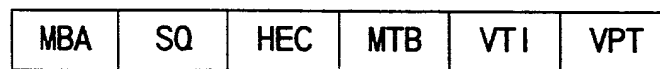


FIG. 28A

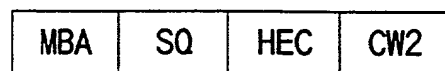
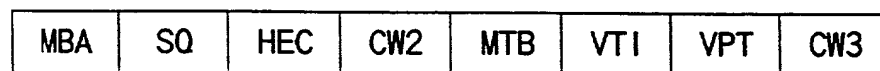


FIG. 28B



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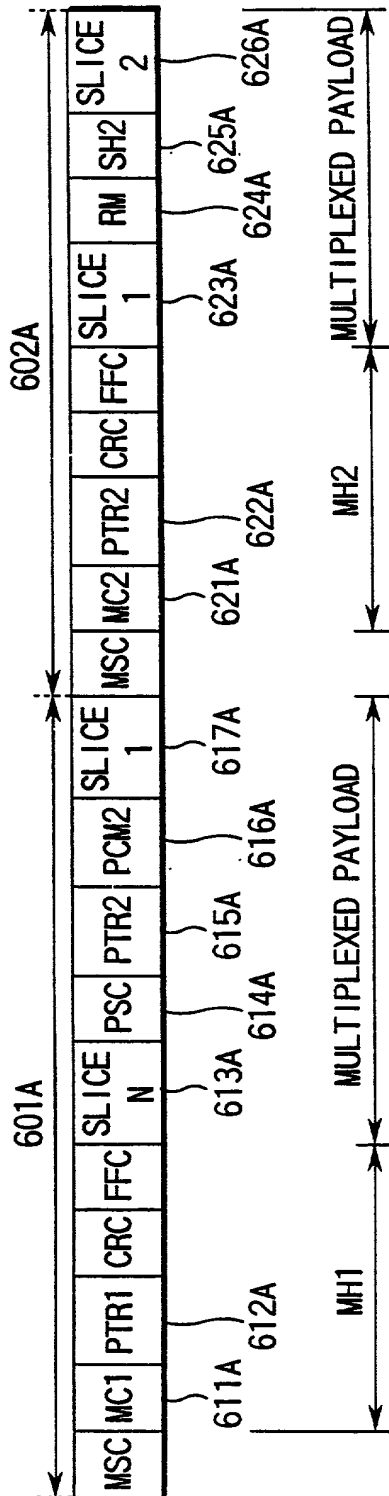


FIG. 25

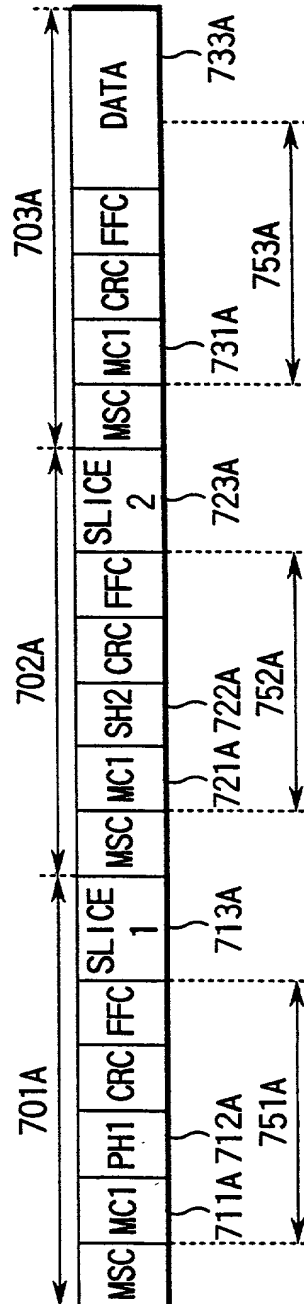


FIG. 26

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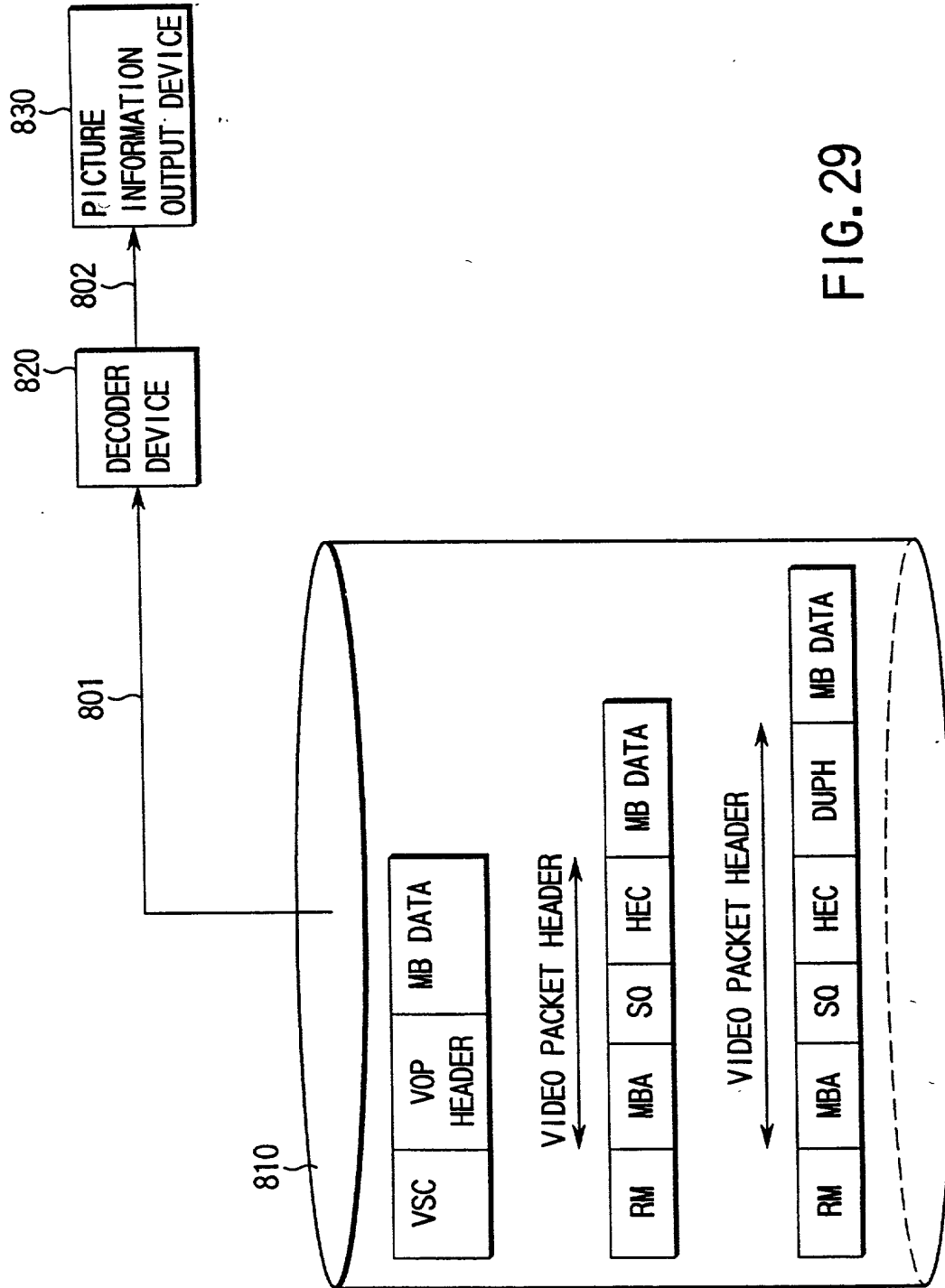


FIG. 29



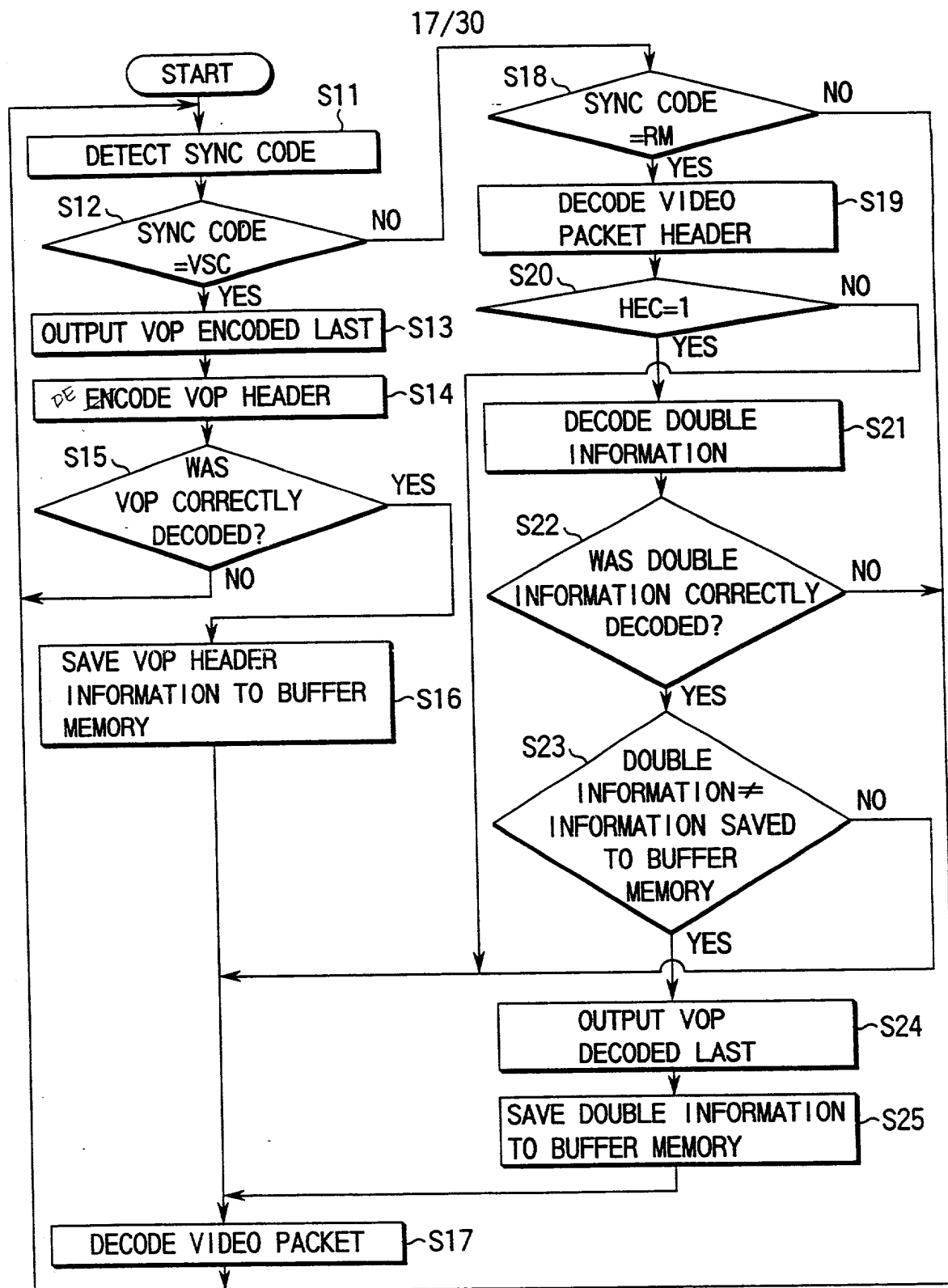


FIG. 30

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FIG. 31A

FIG. 31B



FIG. 31C

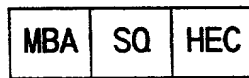


FIG. 31D



FIG. 32A

RESYNC MARKER

000000000000000001

FIG. 32B

VIDEO PACKET HEADER INCLUDING NO MARKER BIT

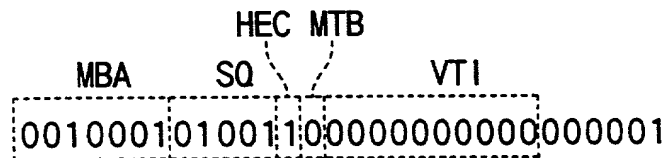


FIG. 32C

VIDEO PACKET HEADER INCLUDING MARKER BIT

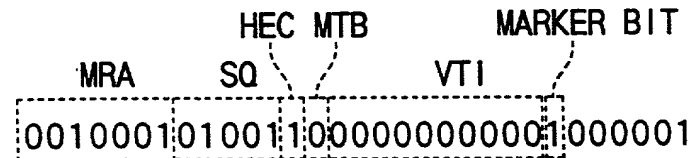


FIG. 33

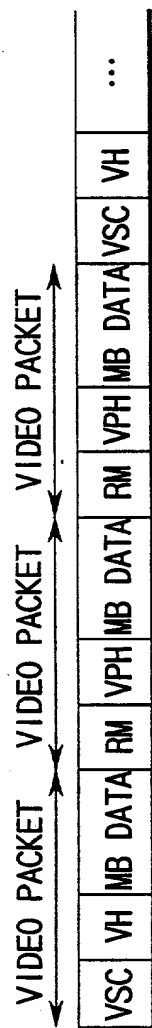


FIG. 34A

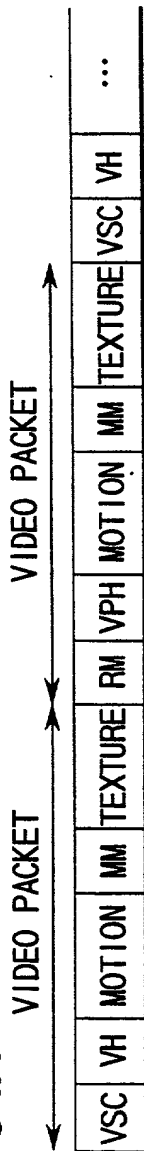


FIG. 34B

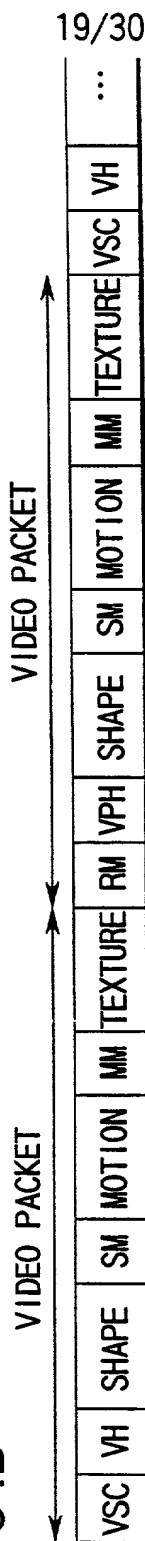


FIG. 34C

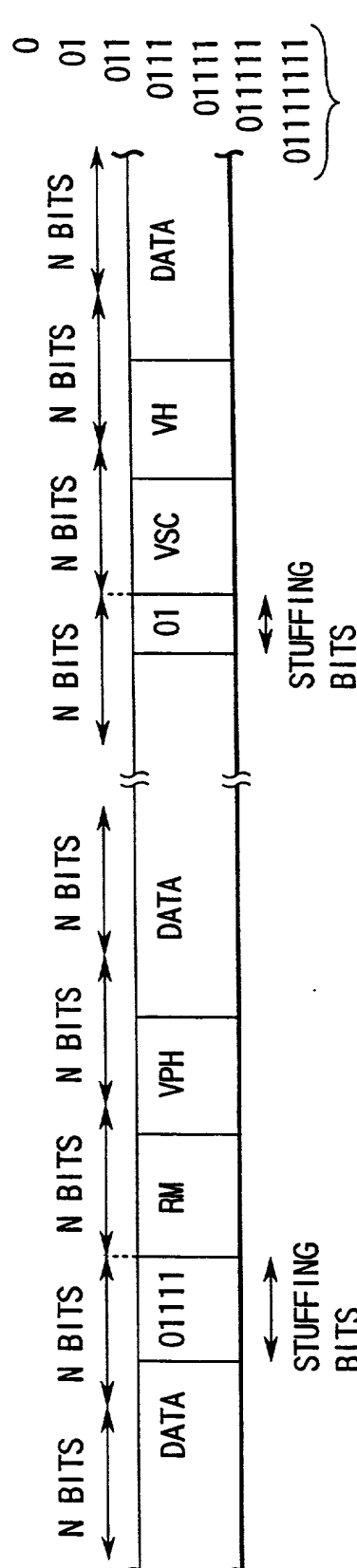


FIG. 35A

FIG. 35B

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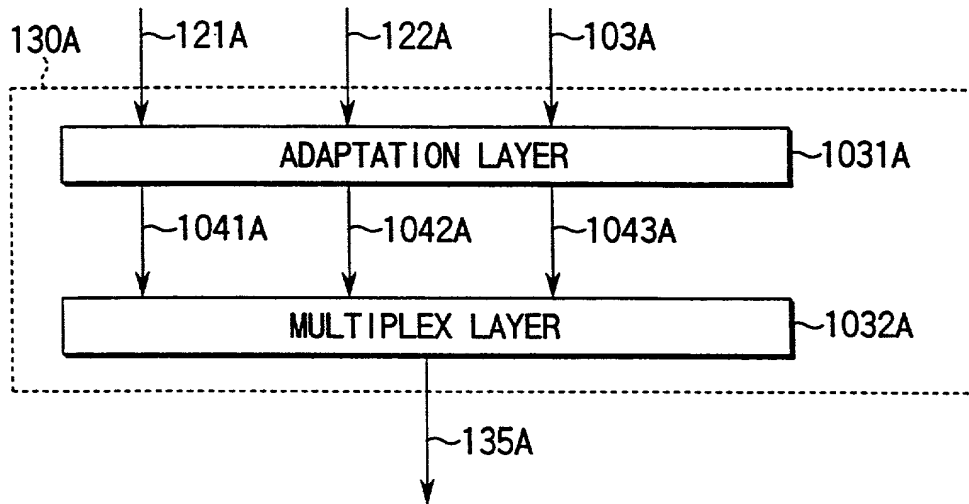


FIG. 36

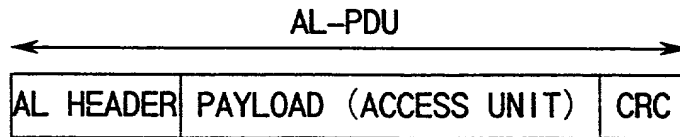
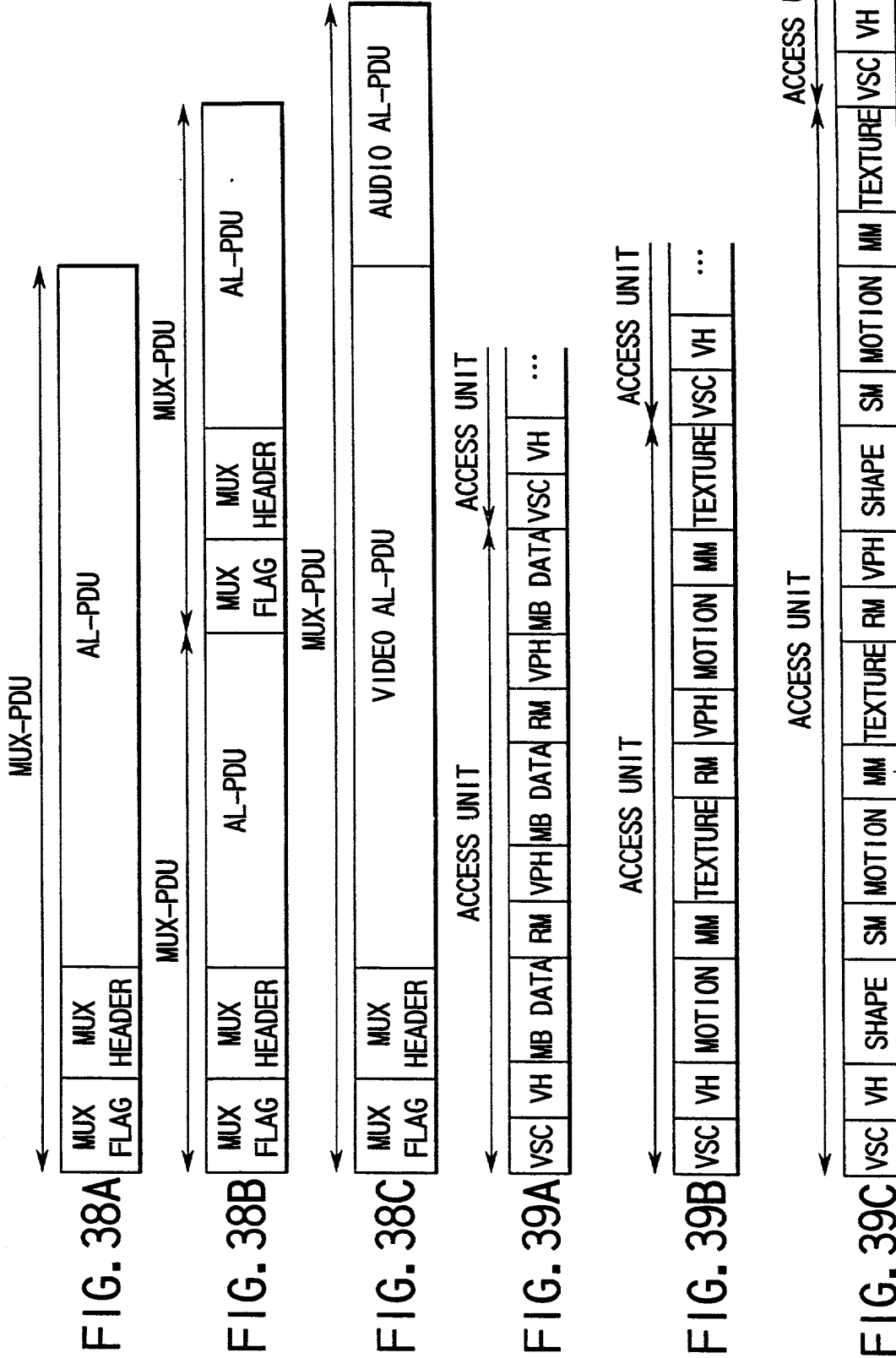


FIG. 37

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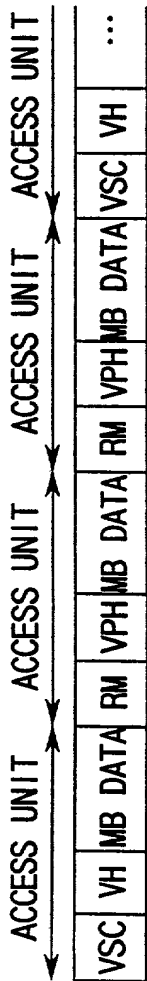


FIG. 40A

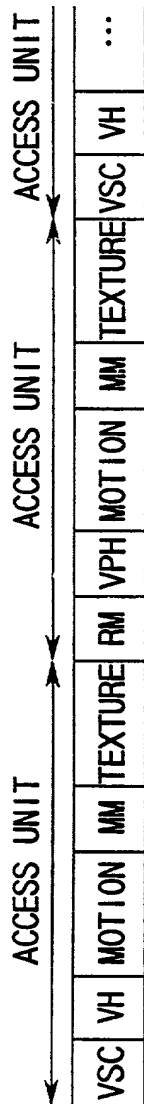


FIG. 40B

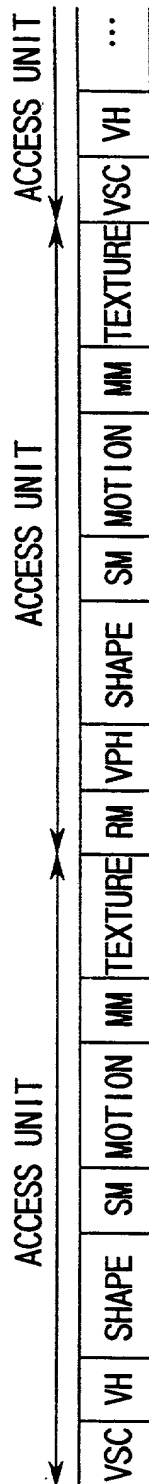


FIG. 40C

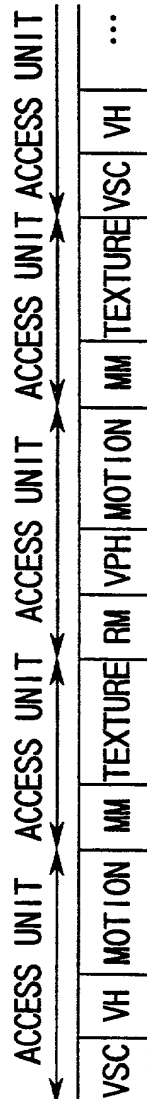


FIG. 41A

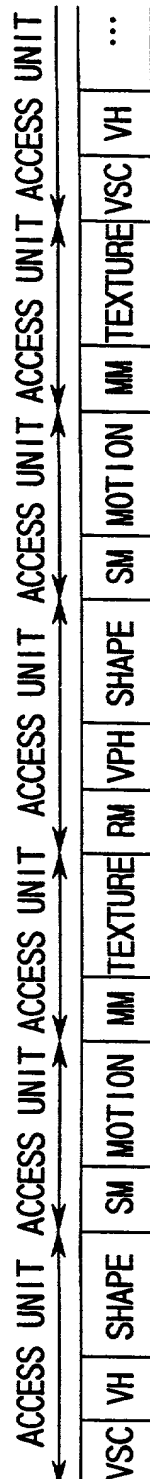


FIG. 41B

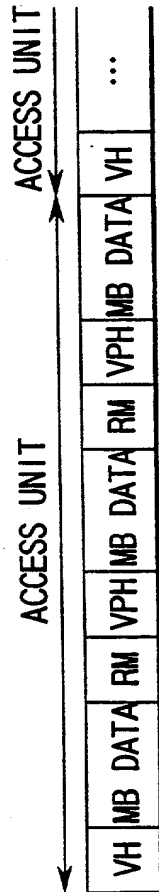


FIG. 42A

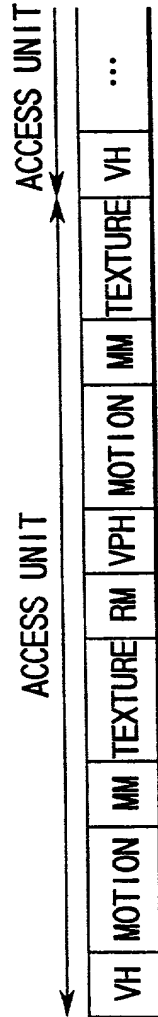


FIG. 42B

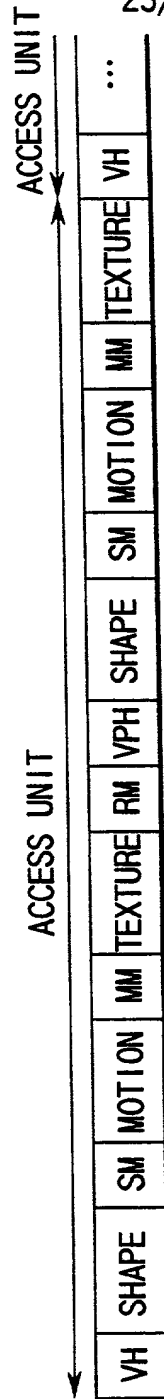


FIG. 42C

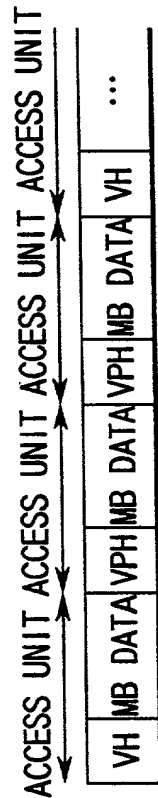


FIG. 43A

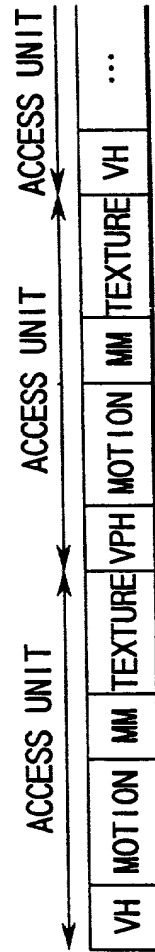


FIG. 43B

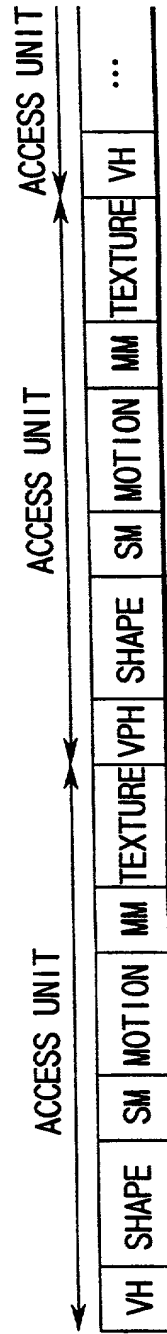


FIG. 43C

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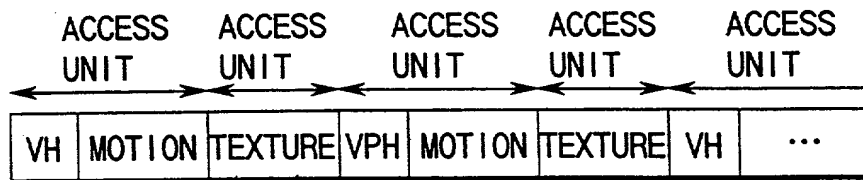


FIG. 44A

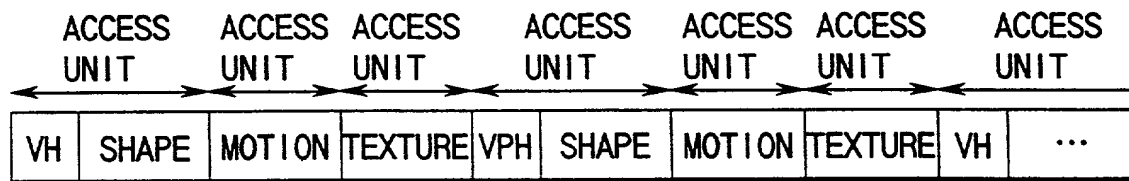


FIG. 44B

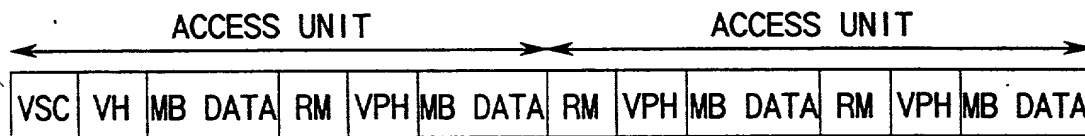


FIG. 45A



FIG. 45B

20130808 14:00:00



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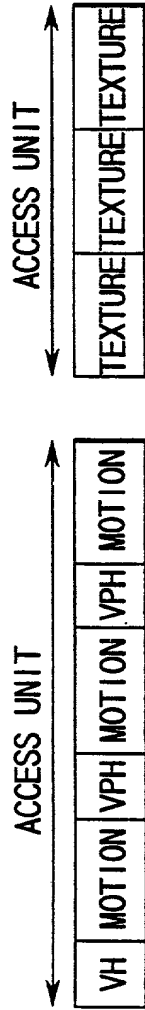


FIG. 46A

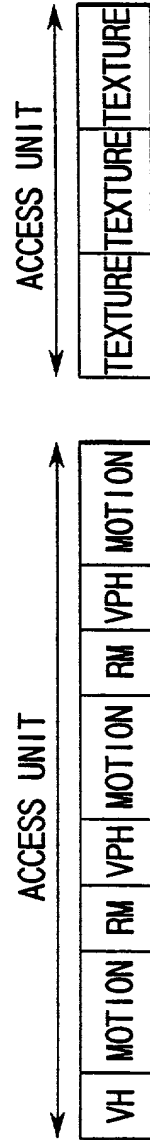


FIG. 46B

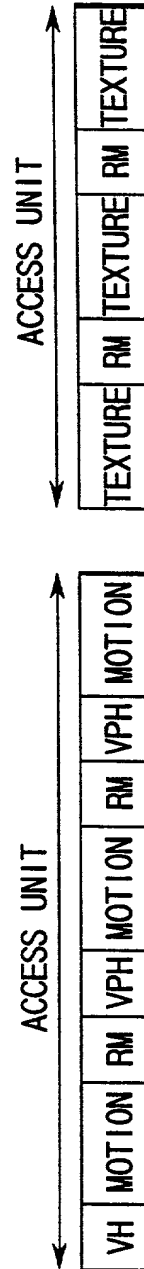


FIG. 46C

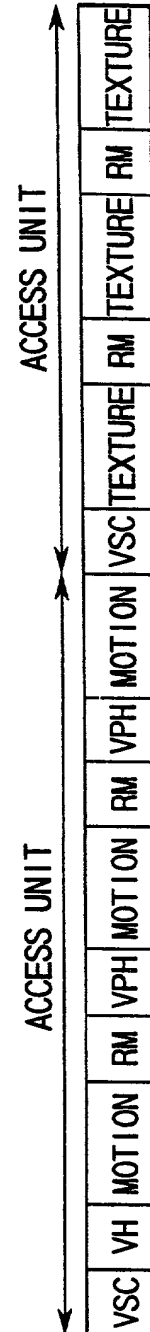


FIG. 46D

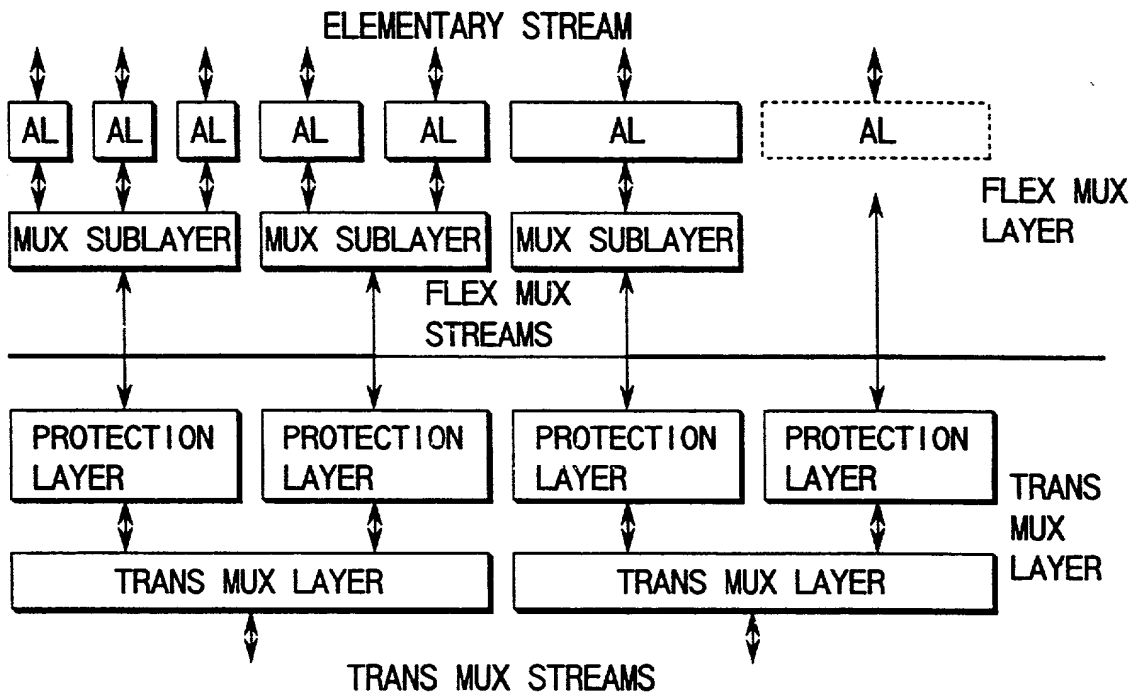
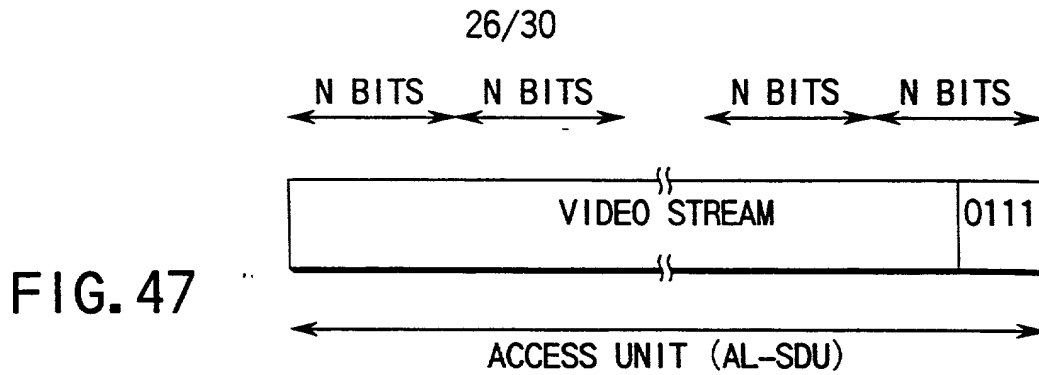
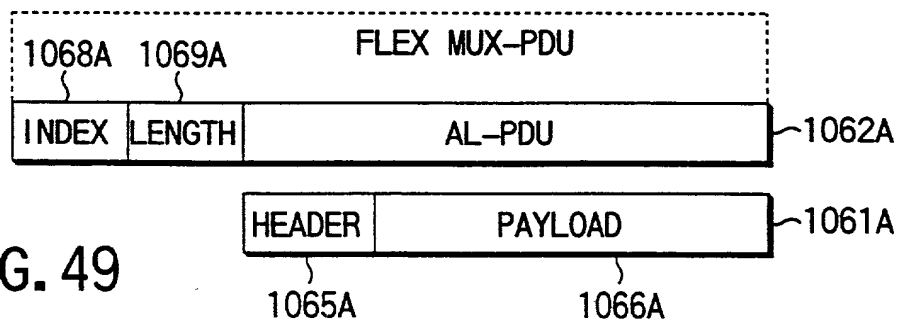
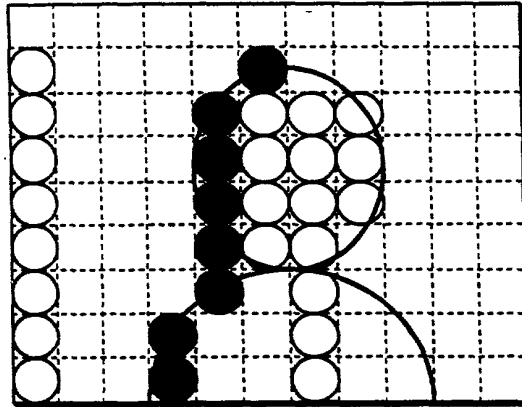


FIG. 48

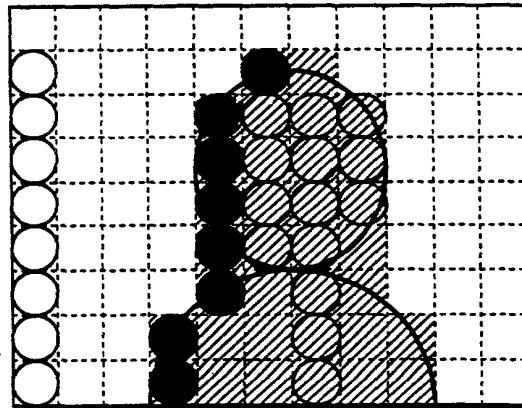


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- ACCESS UNIT BOUNDARY
- RE-SYNC MARKER

FIG. 50



- ACCESS UNIT BOUNDARY
- RE-SYNC MARKER
- /// HIGH QoS

FIG. 51

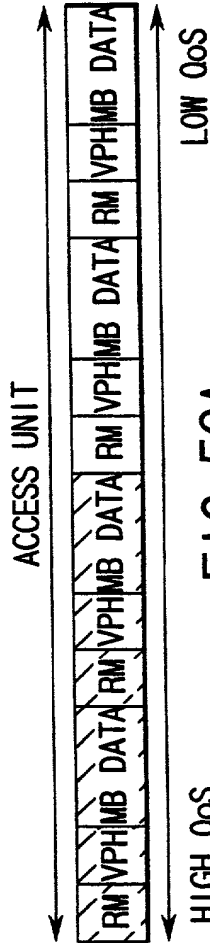


FIG. 52A

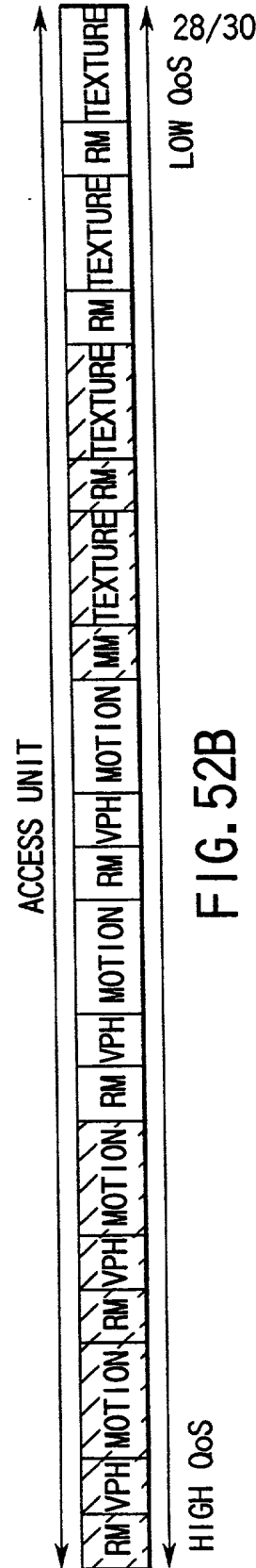


FIG. 52B

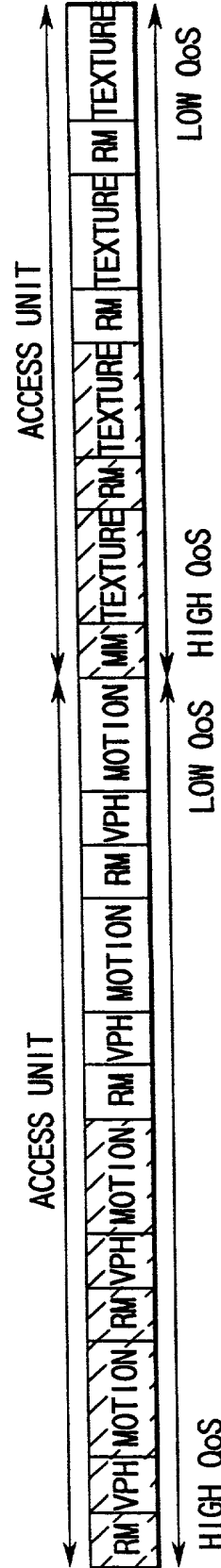
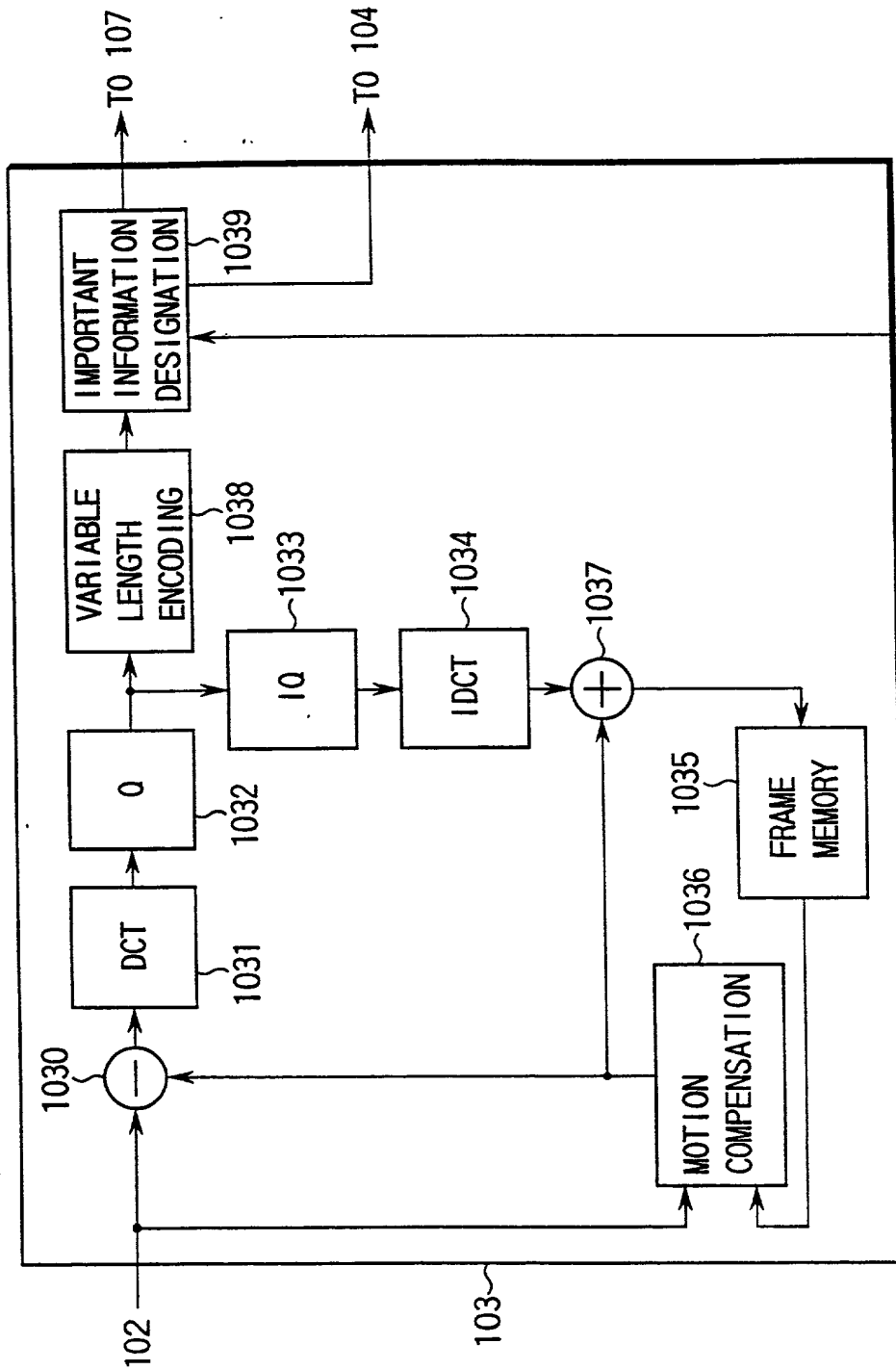


FIG. 52C

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DESIGNATION OF  
IMPORTANT INFORMATION

FIG. 53

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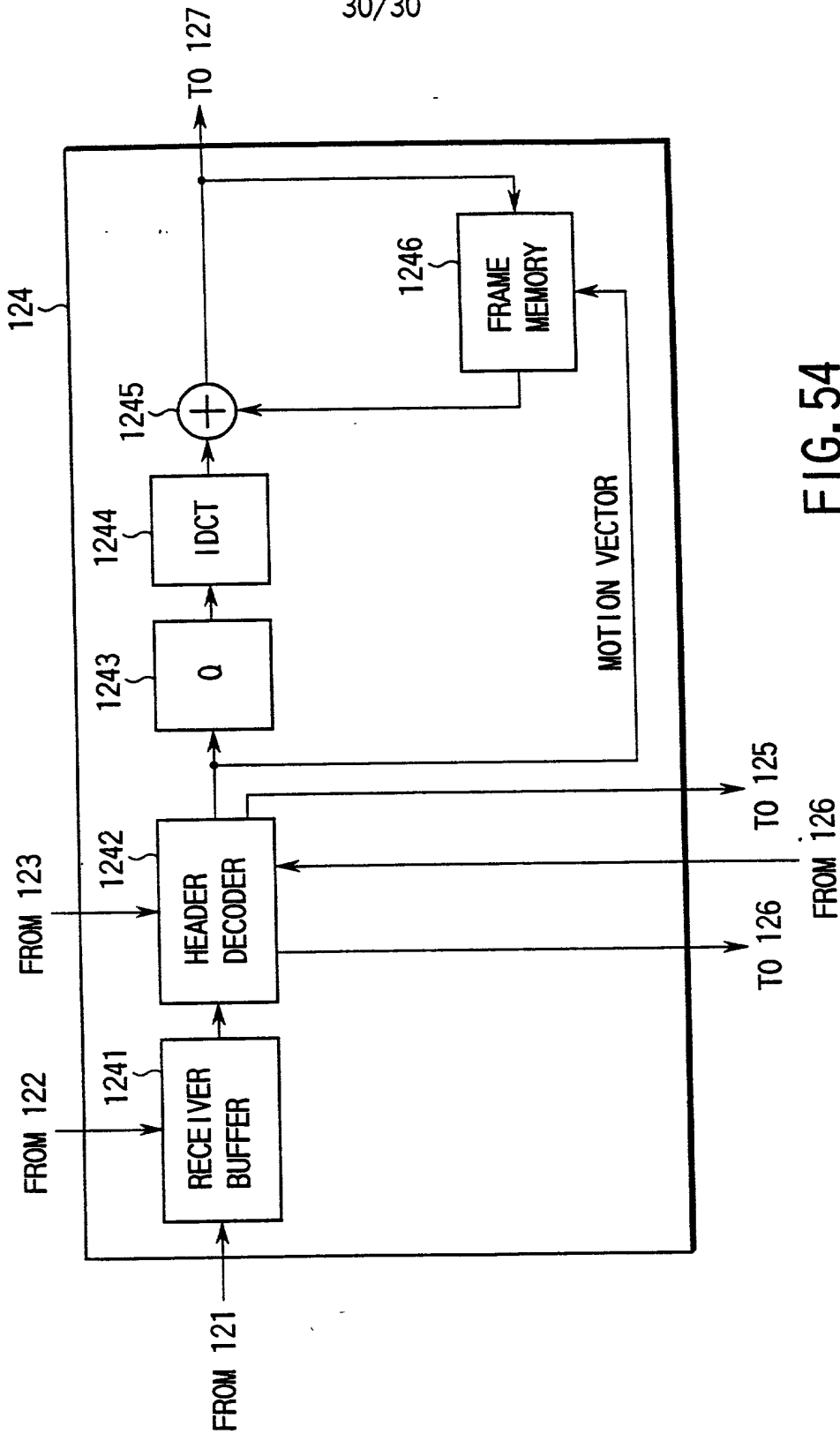


FIG. 54